Documentation for CTT/PS Protocols v07-00 is now complete and published

d0server1.fnal.gov/projects/VHDL/General/

- √ Tracks to L1Muon via FSCL
- ✓ CFT/CPSax & FPS TT to TM via FSCL
- √ CFT/CPSax L1 Trigger Terms
- ✓ All FPS L1 & L2 LVDS and G-Link
- √ FPS L1 Trigger Terms
- ✓ All CFT/CPSax/STT L1 & L2 LVDS
- ✓ All CFT/CPSax/STT G-Link to L2pp
- ✓ L3/VRB readout via G-Link
- ✓ Extensive descriptions of bitfields including all the many conventions...
- ✓ Lots of changes, minor and major, ...

Thanks to all for your help and support!

Comments, suggestions, ... direct them to me: blevan@fnal.gov asap